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Important Deadlines

Full Papers Submission: May 1, 2009

Notification of Acceptance: June 26, 2009

Camera Ready Copy: July 31, 2009

Author's Registration: July 31, 2009

Advance Registration: August 28, 2009



2009 IEEE Toronto International Conference - Science and Technology for Humanity (TIC-STH 2009)

<http://www.TIC-STH2009.org>

September 26-27 2009, Toronto Marriott Downtown Eaton Centre, Toronto, Canada

Symposium on Electronic Design Automation

Call for Papers

This symposium aims at providing researchers with opportunities for presenting recent advances in technologies related to Electronic Design Automation (EDA). Papers are invited in the areas related to EDA, including, but not limited to, the following topics:

- System-level Design and Co-Design
- Embedded Hardware Design and Applications
- Physical Design (Routing and Placement)
- High-Level Synthesis, Logic Synthesis, and Circuit Optimization
- FPGA Design Tools and Applications
- Timing, Power, and Thermal Analysis and Optimization
- Embedded Software Tools and Design

All submissions must conform to the TIC-STH 2009 submission policy and should be in English language.

Symposium Technical Program Committee

Chair: Gary Grewal, University of Guelph

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